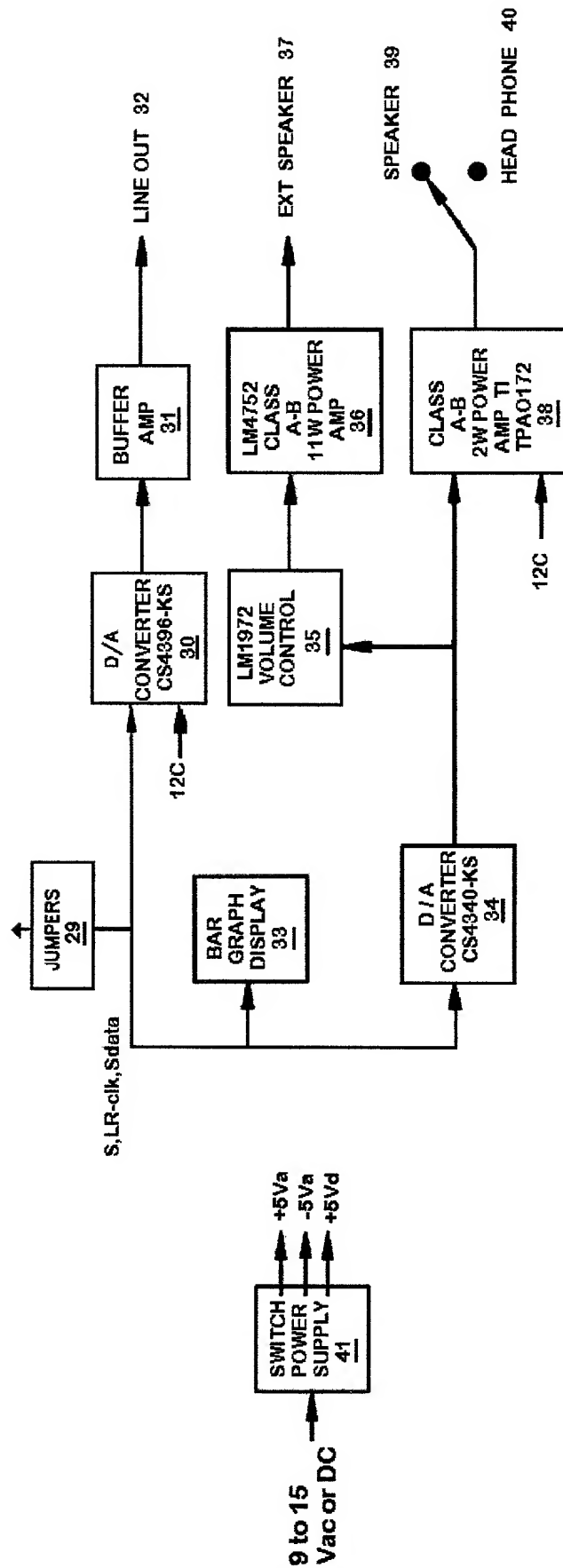
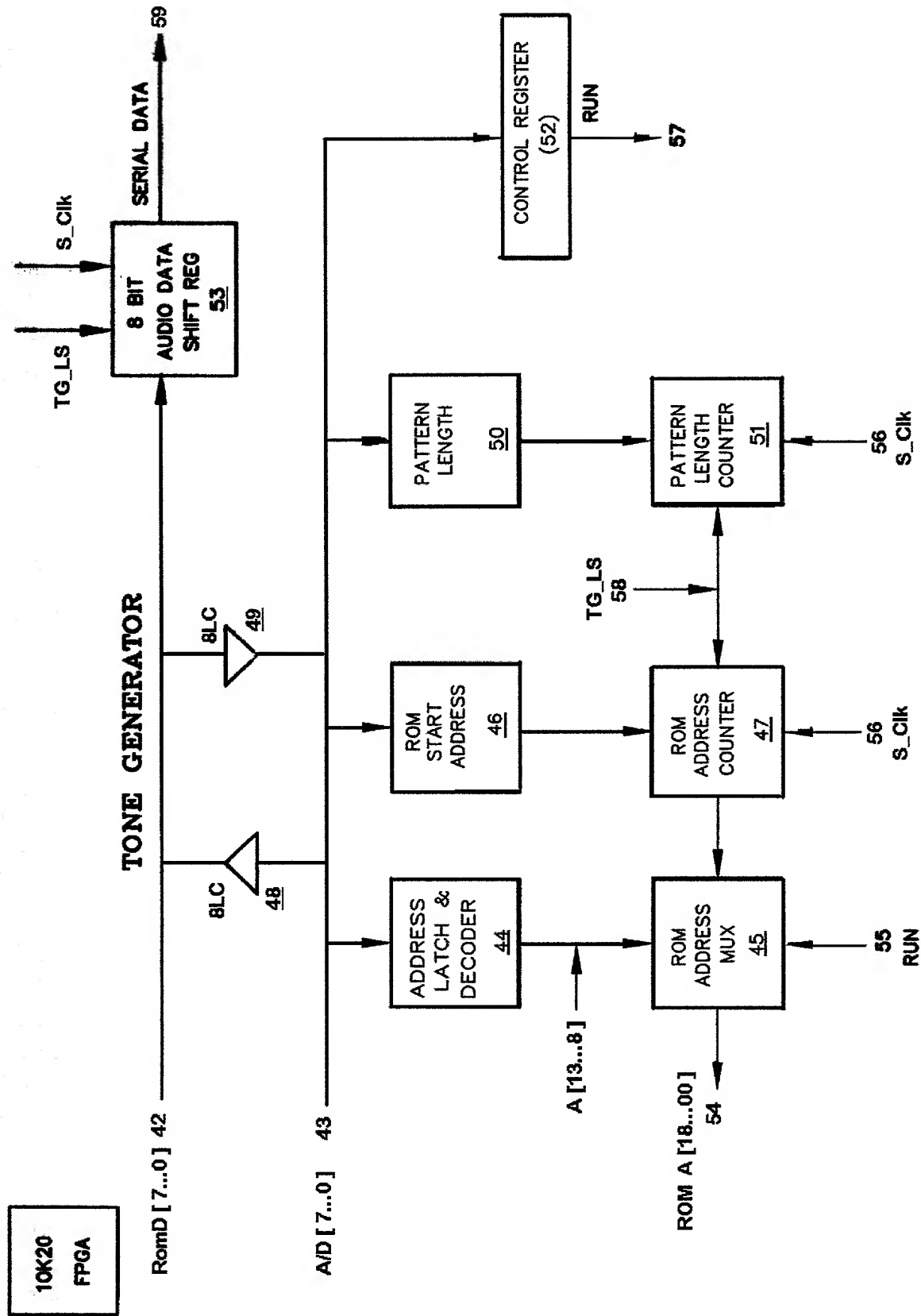


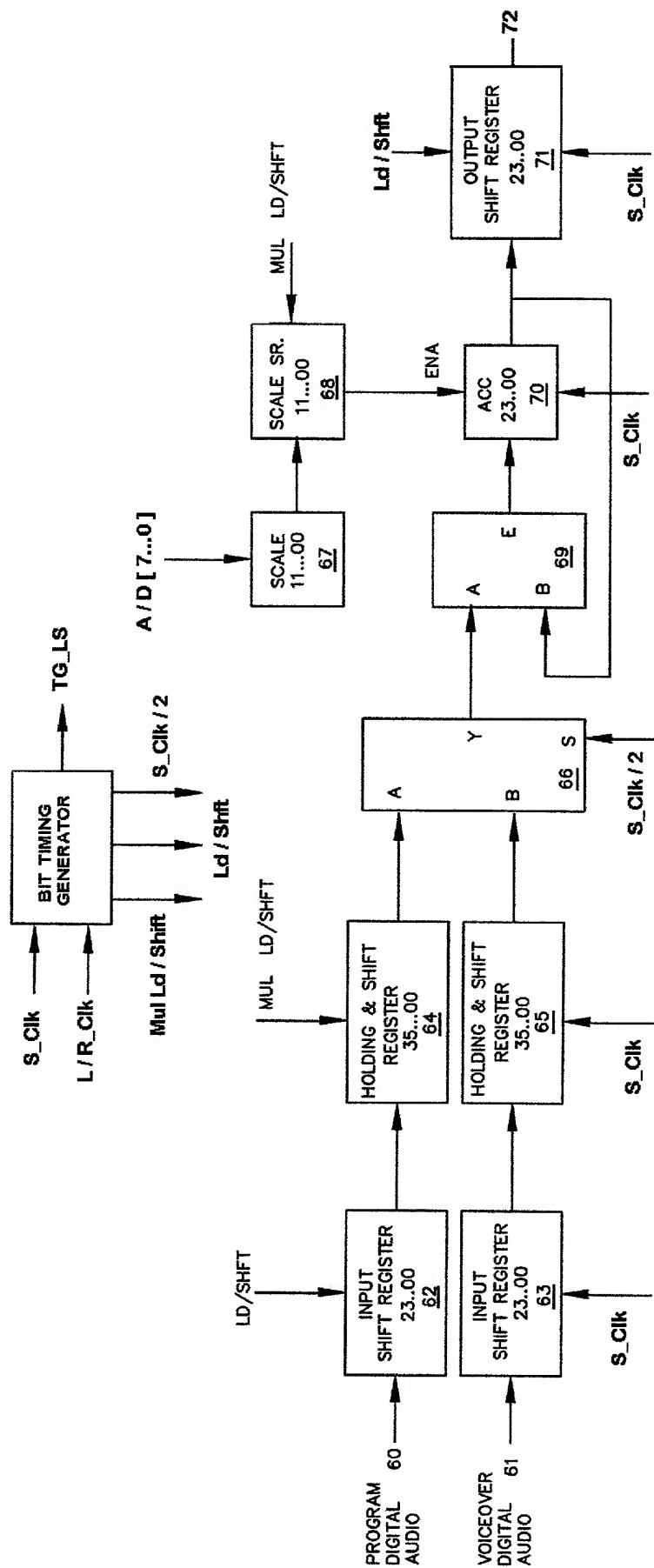
Fig 1a-A



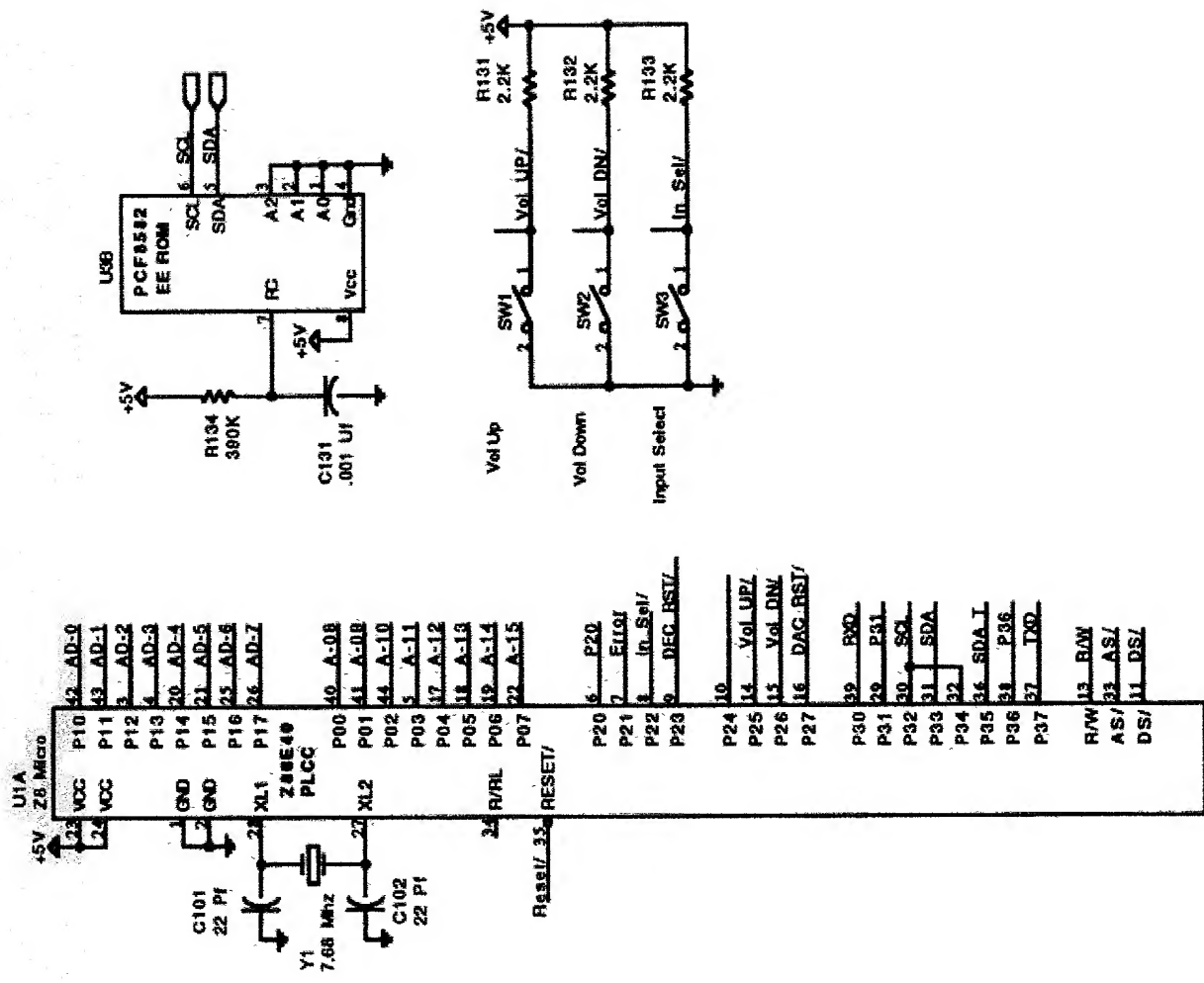
Audio Display Voice Over & Tone
Overall Block Diagram
Fig. 1b



Audio Display Voice Over & Tone
Inside the Gene FPGA
Fig. 2

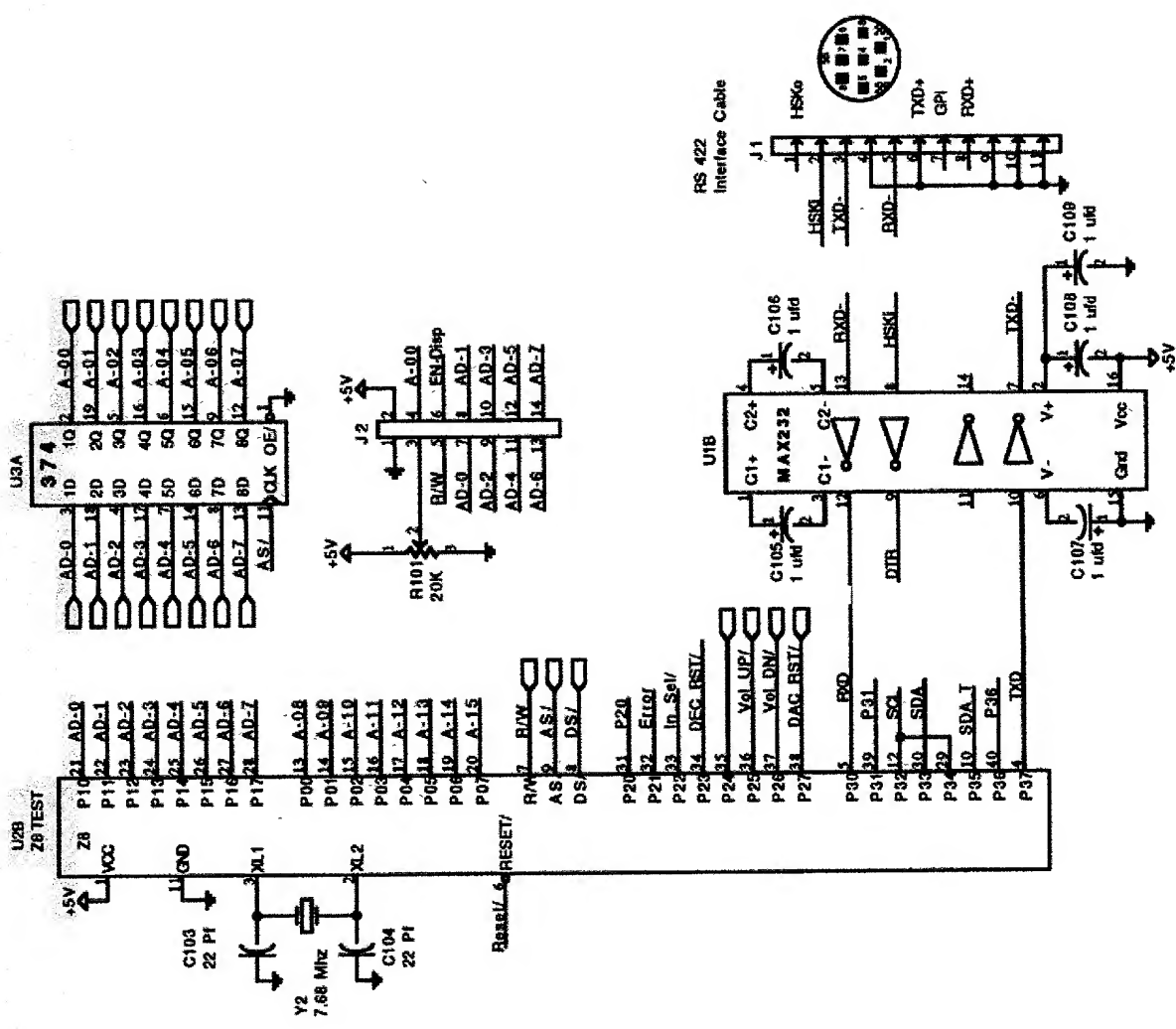


Audio Display Voice Over & Tone
2 Channel Stereo Audio Disolver \ Mixer
Fig 3

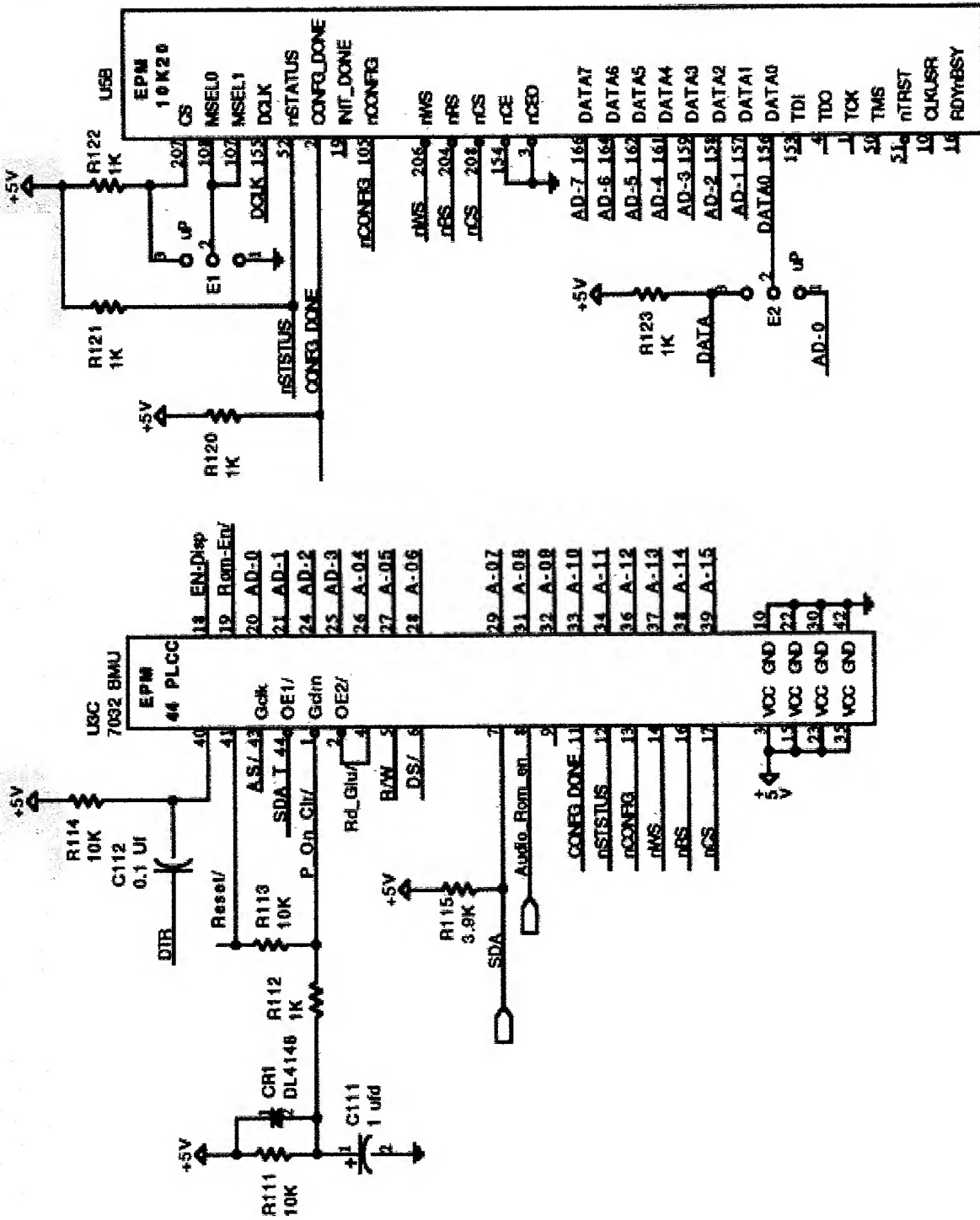


Audio Display Voice Over & Tone
Computer Interface
Fig 4a

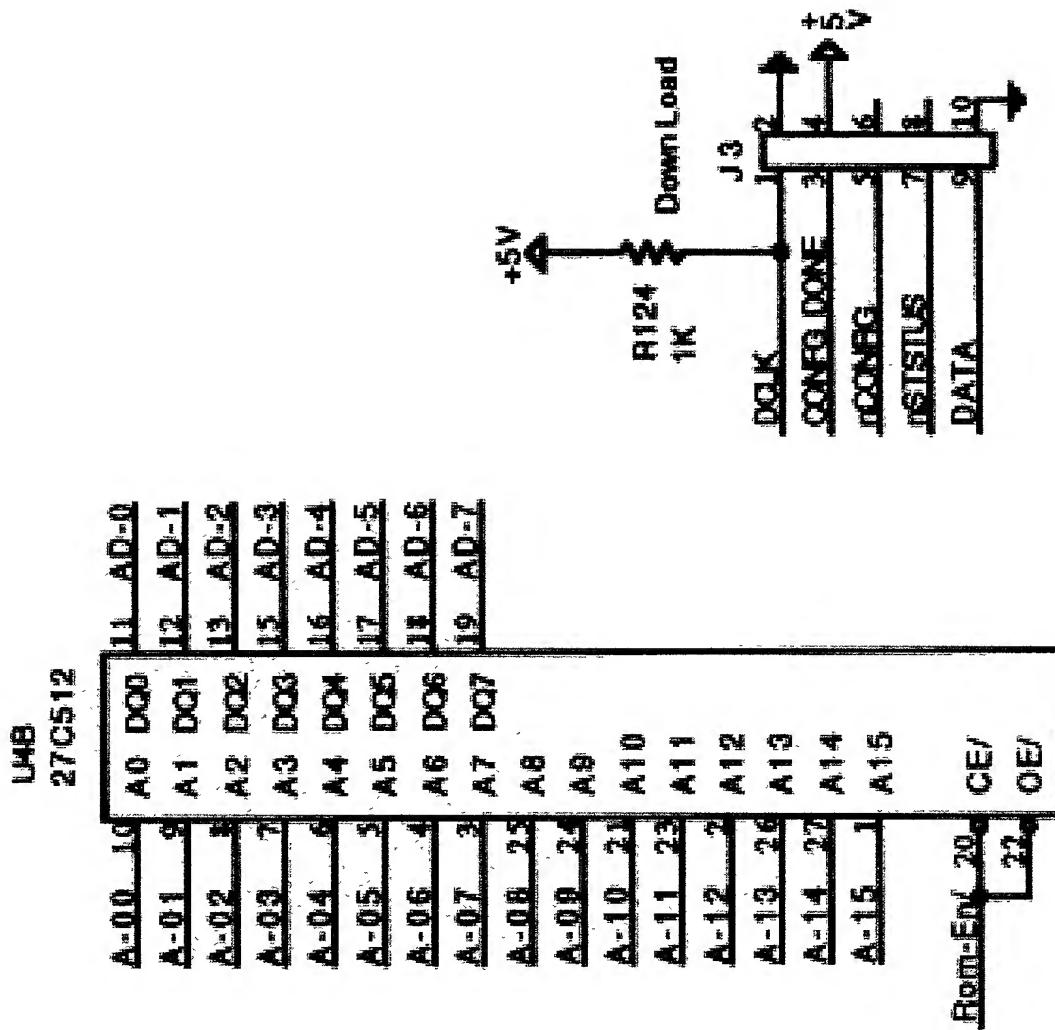
www.9000.com



Audio Display Voice Over & Tone
Computer Interface
Fig 4b

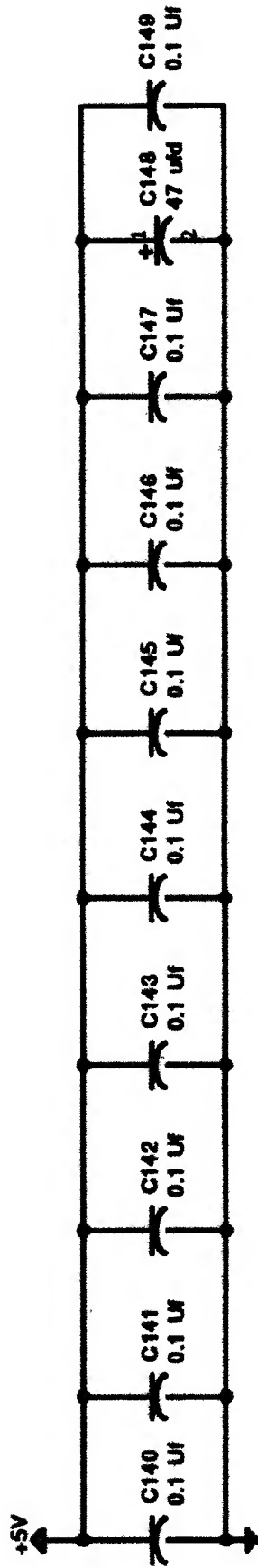


Audio Display Voice Over & Tone
Computer Interface
Fig 4c

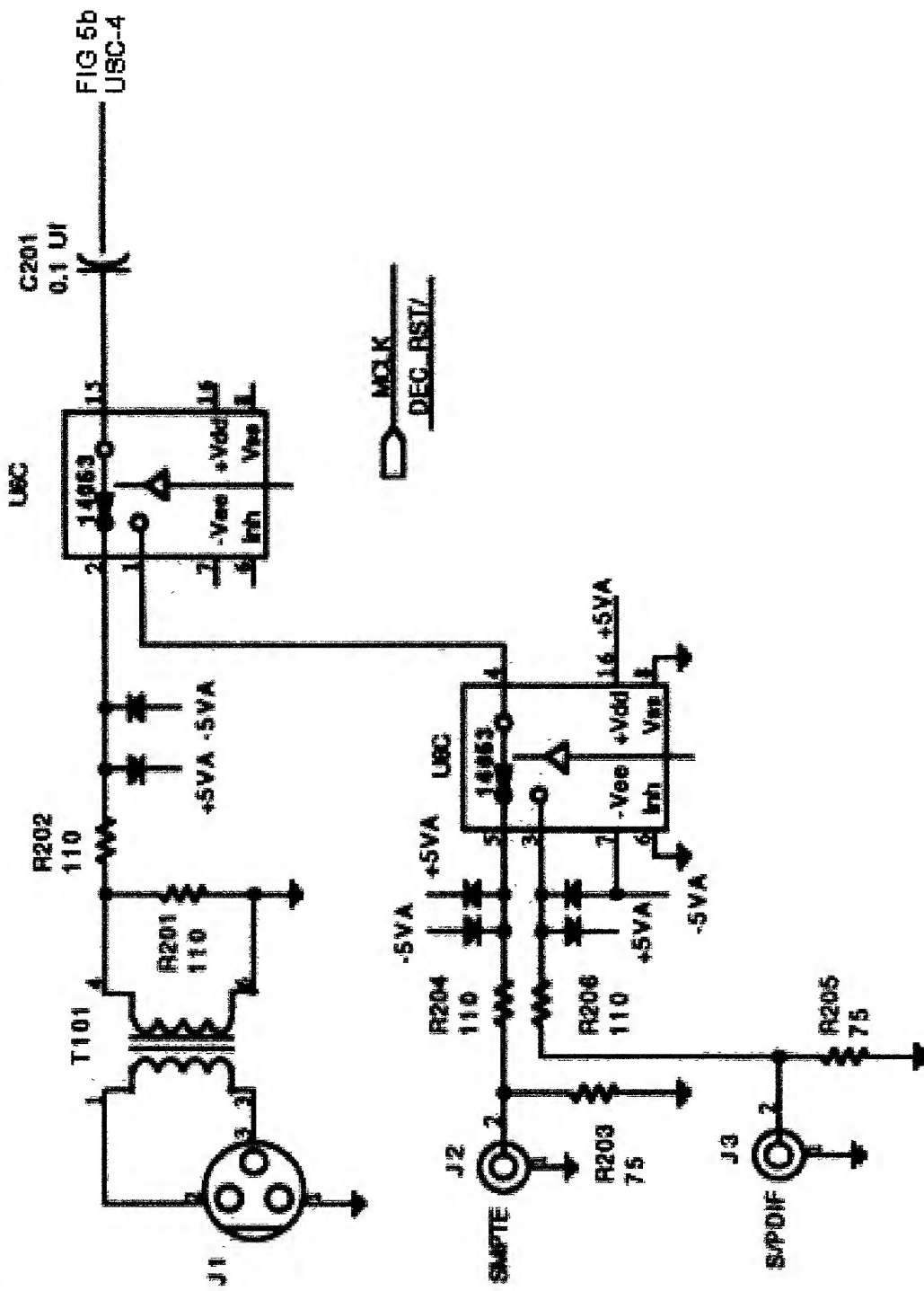


Audio Display Voice Over & Tone
Computer Interface
Fig 4d

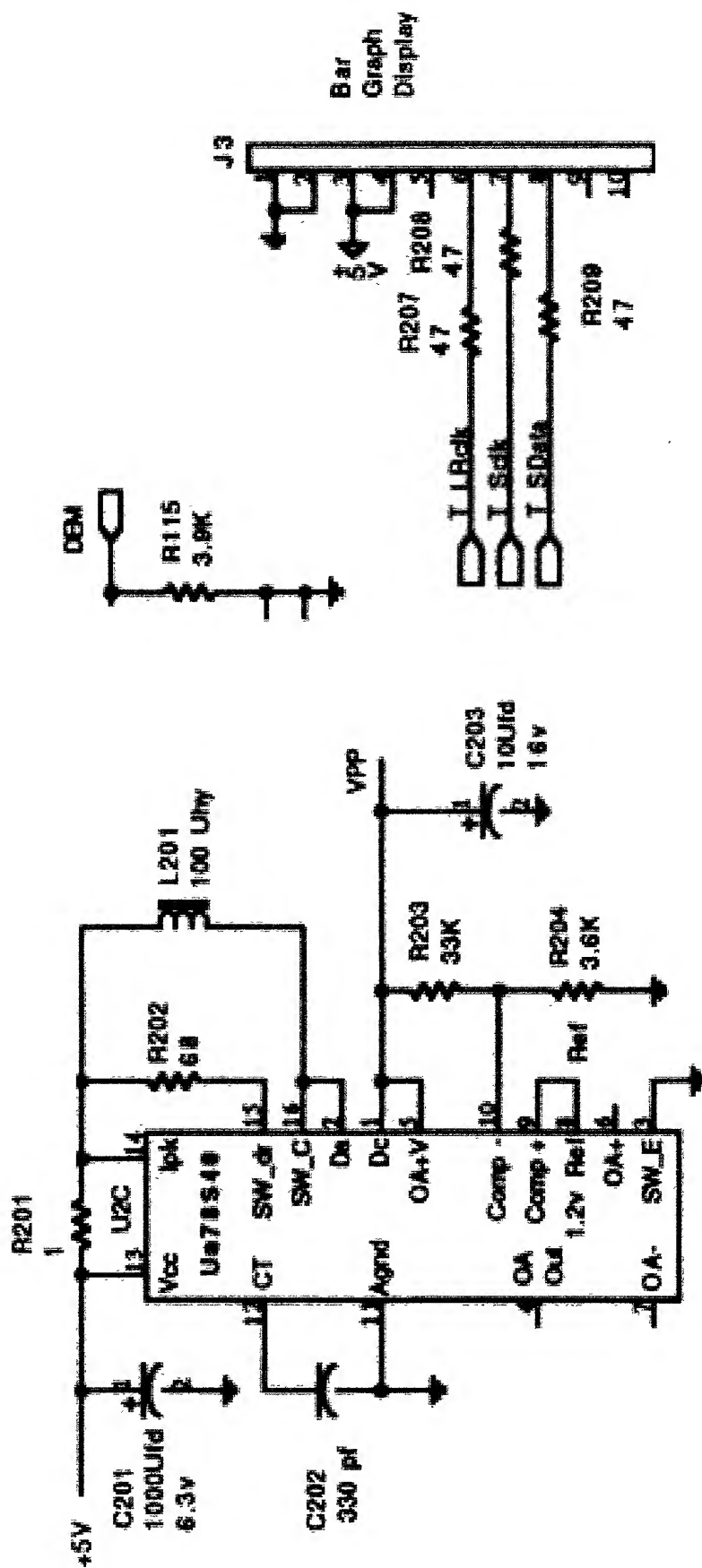
www.CircuitScreener.com



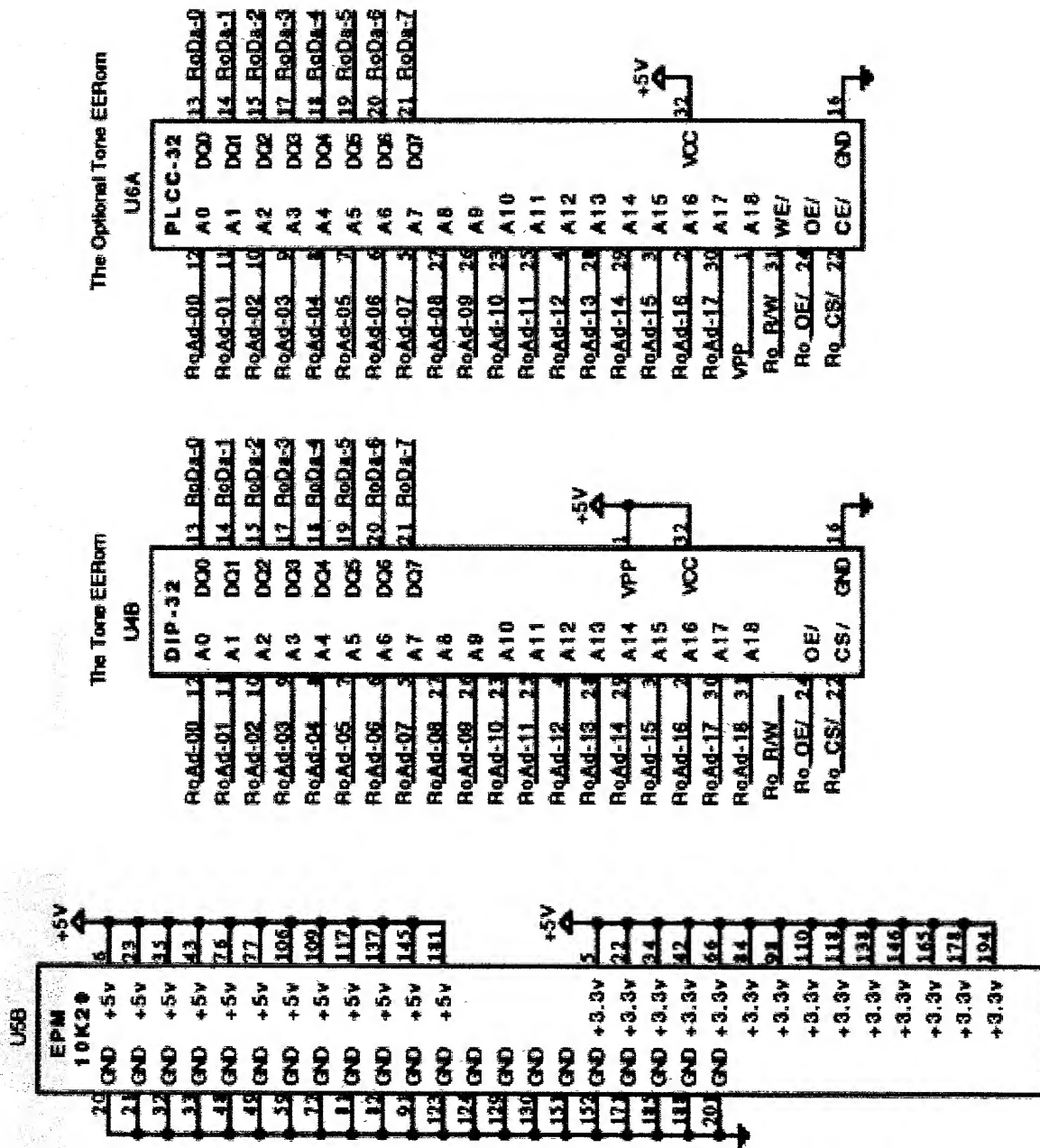
Audio Display Voice Over & Tone
Computer Interface
Fig 4e



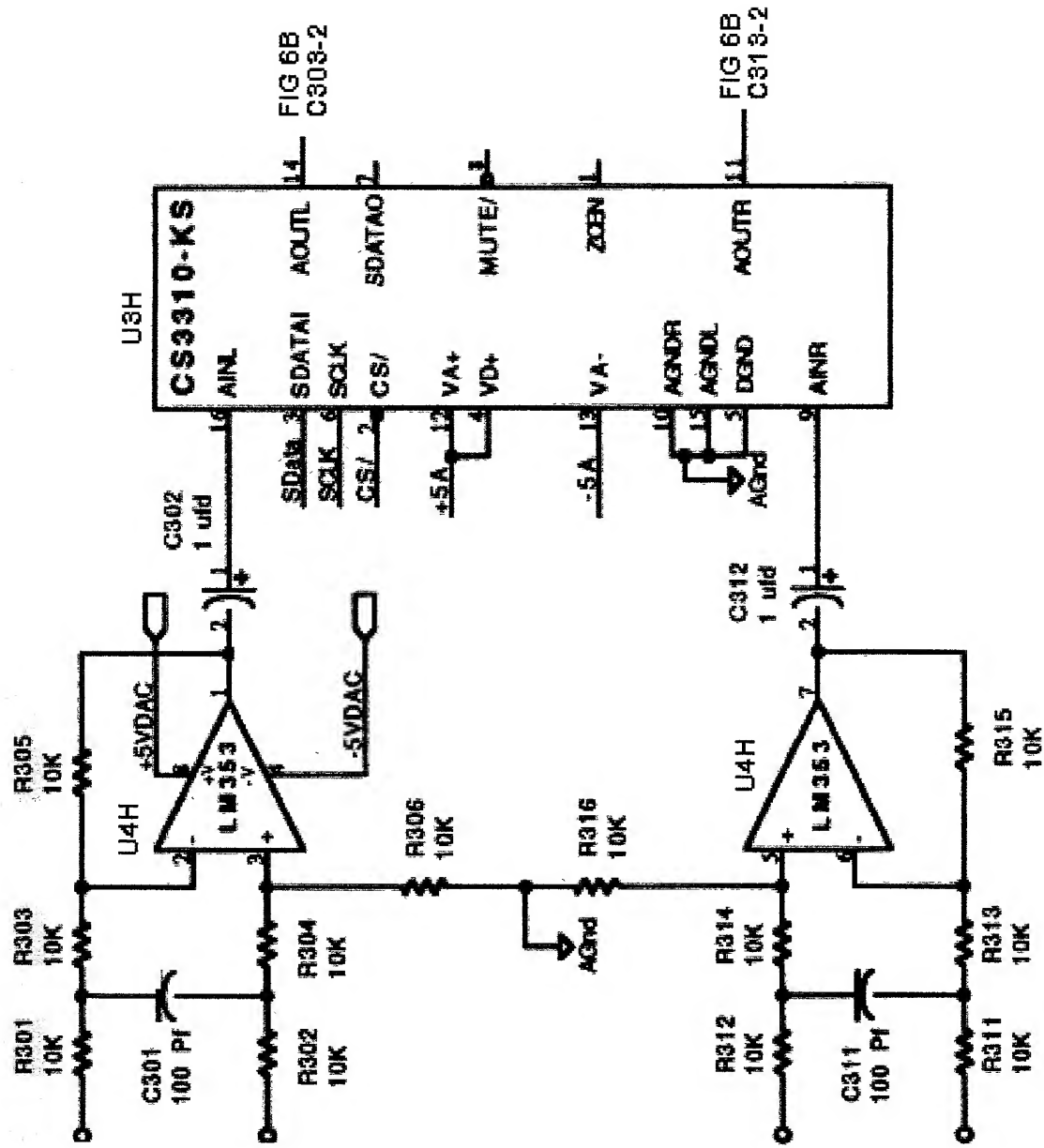
Audio Display Voice Over & Tone
Digital Audio
Fig 5a



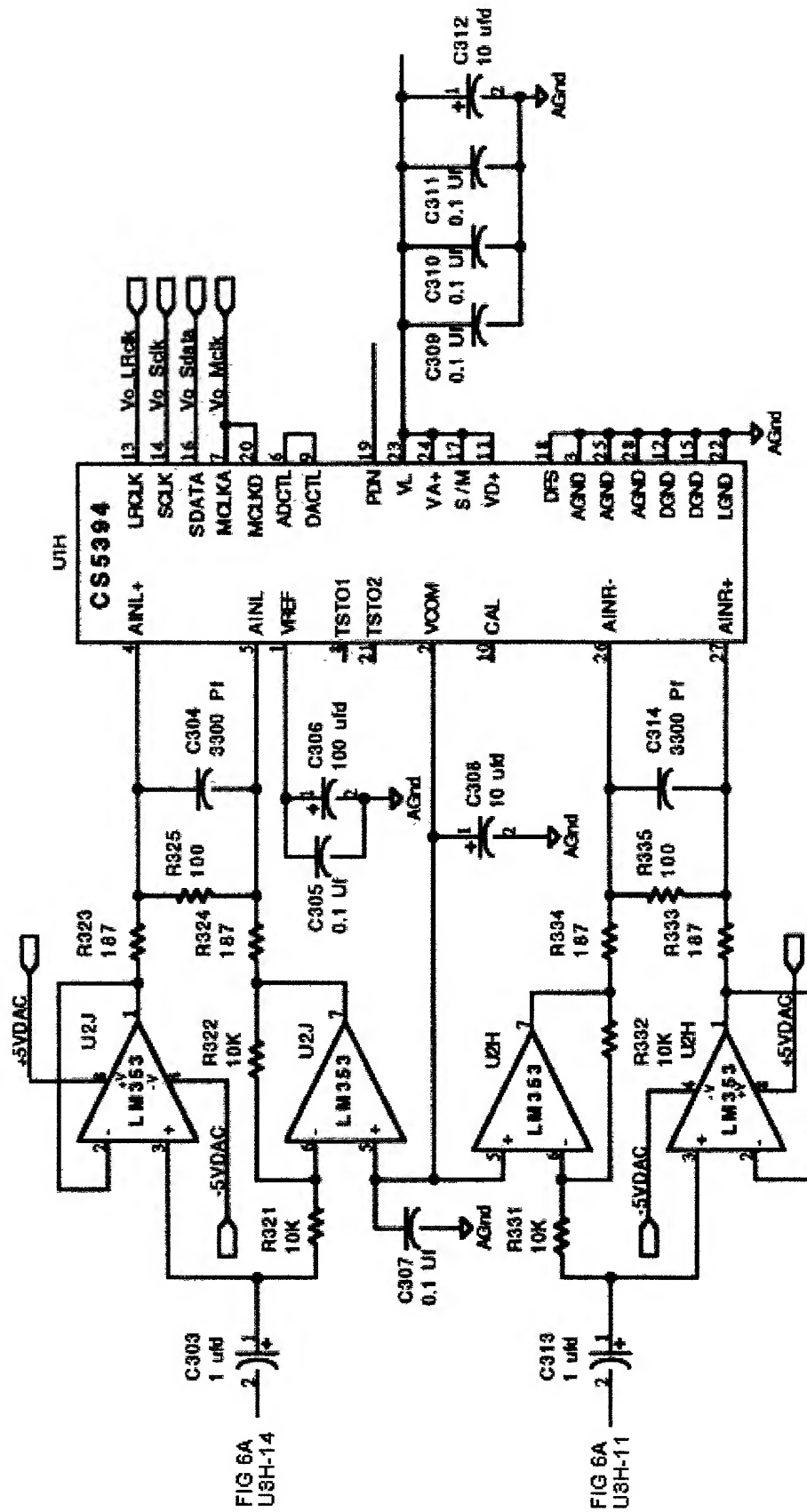
Audio Display Voice Over & Tone
Digital Audio
Fig 5c

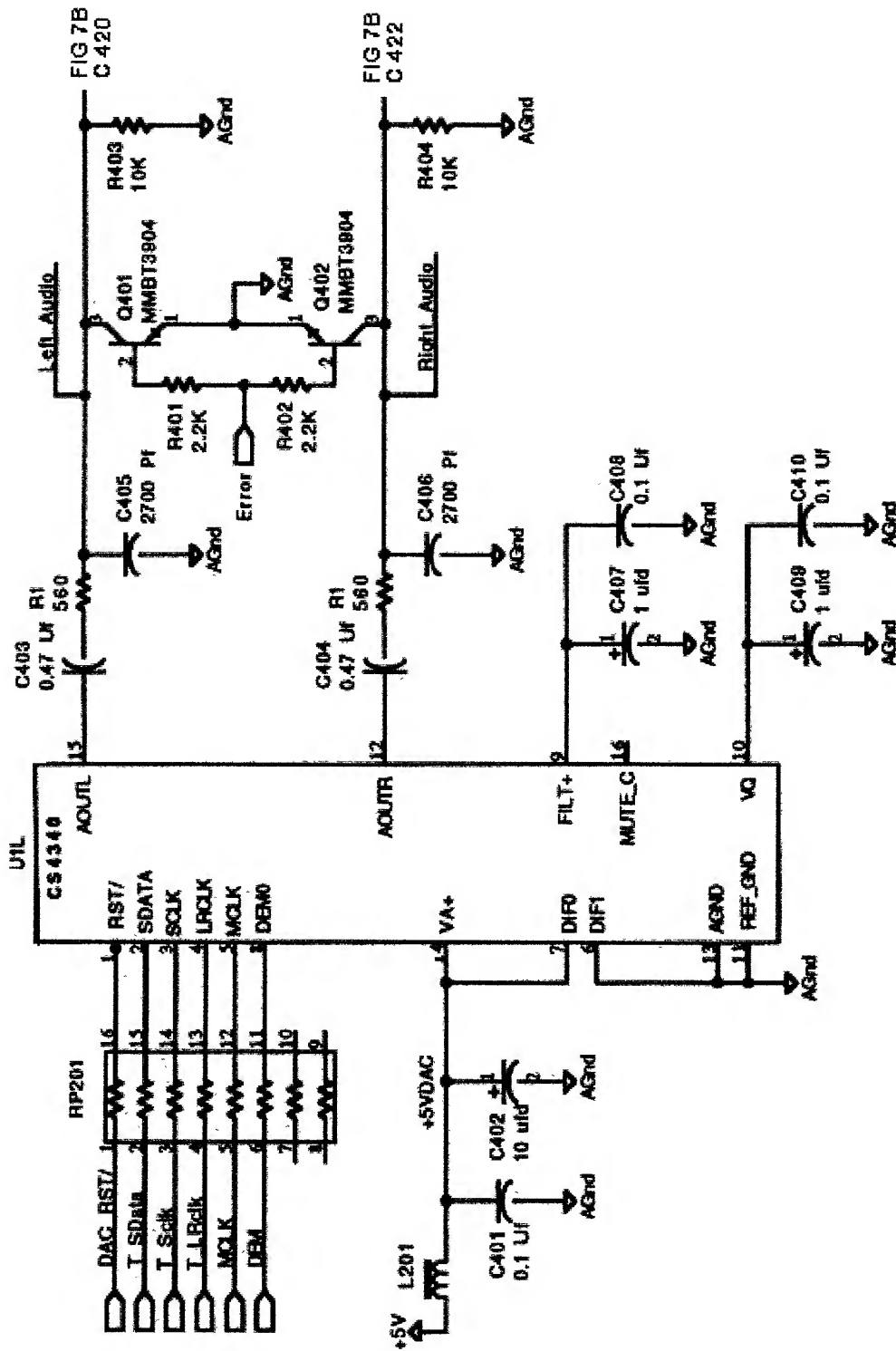


Audio Display Voice Over & Tone
Digital Audio
Fig 5e

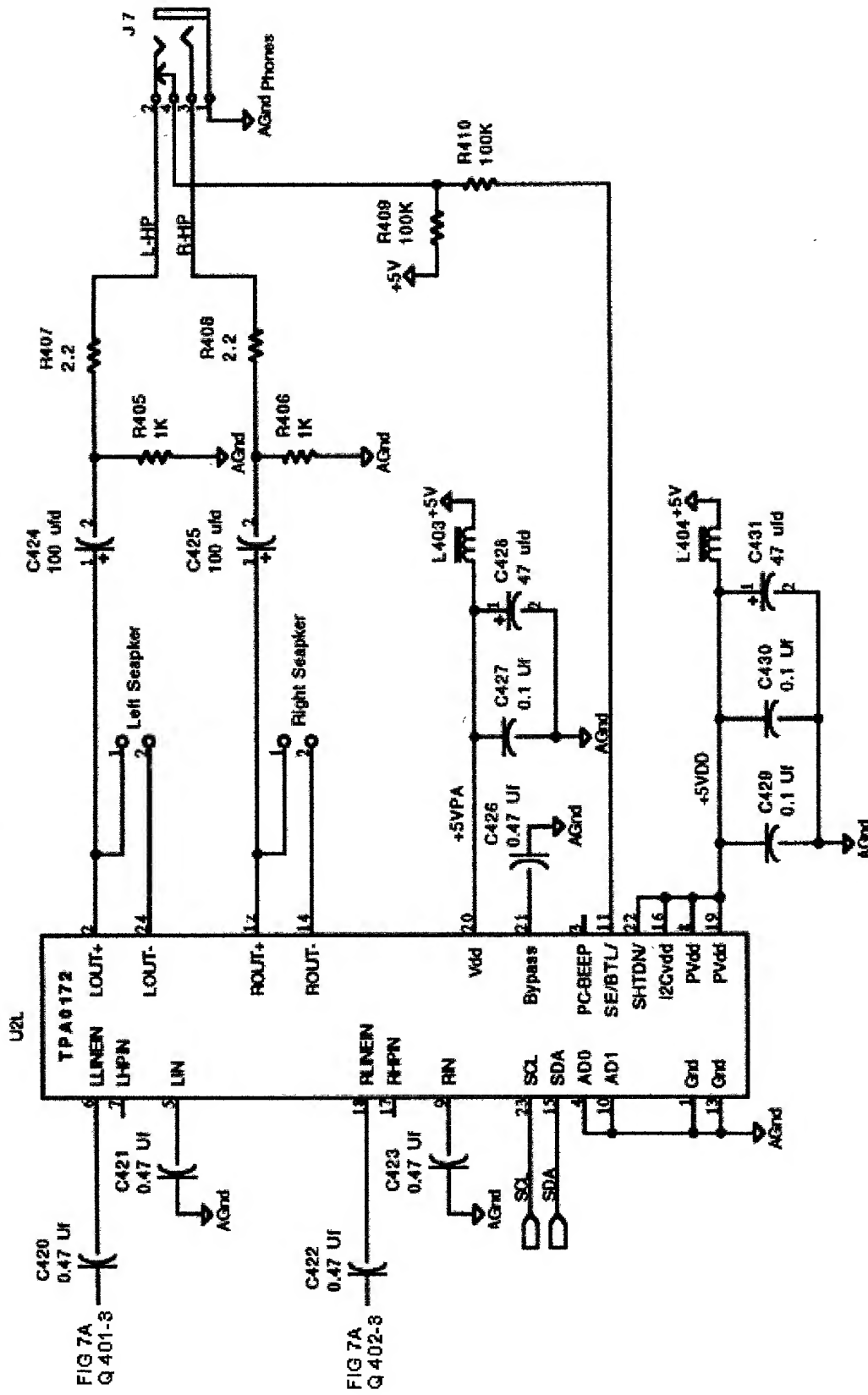


Audio Display Voice Over & Tone
Analog Audio Output
Fig 6a

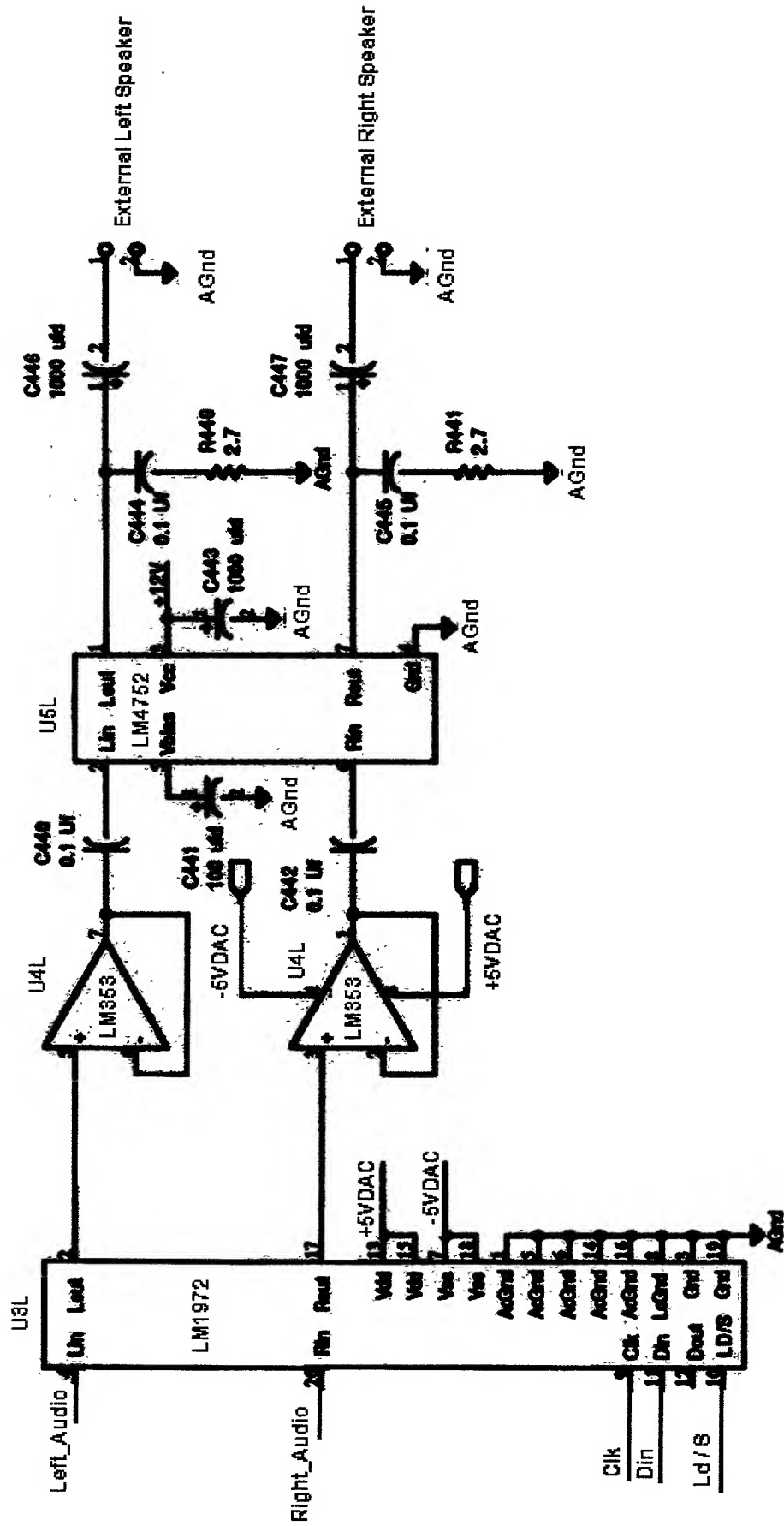




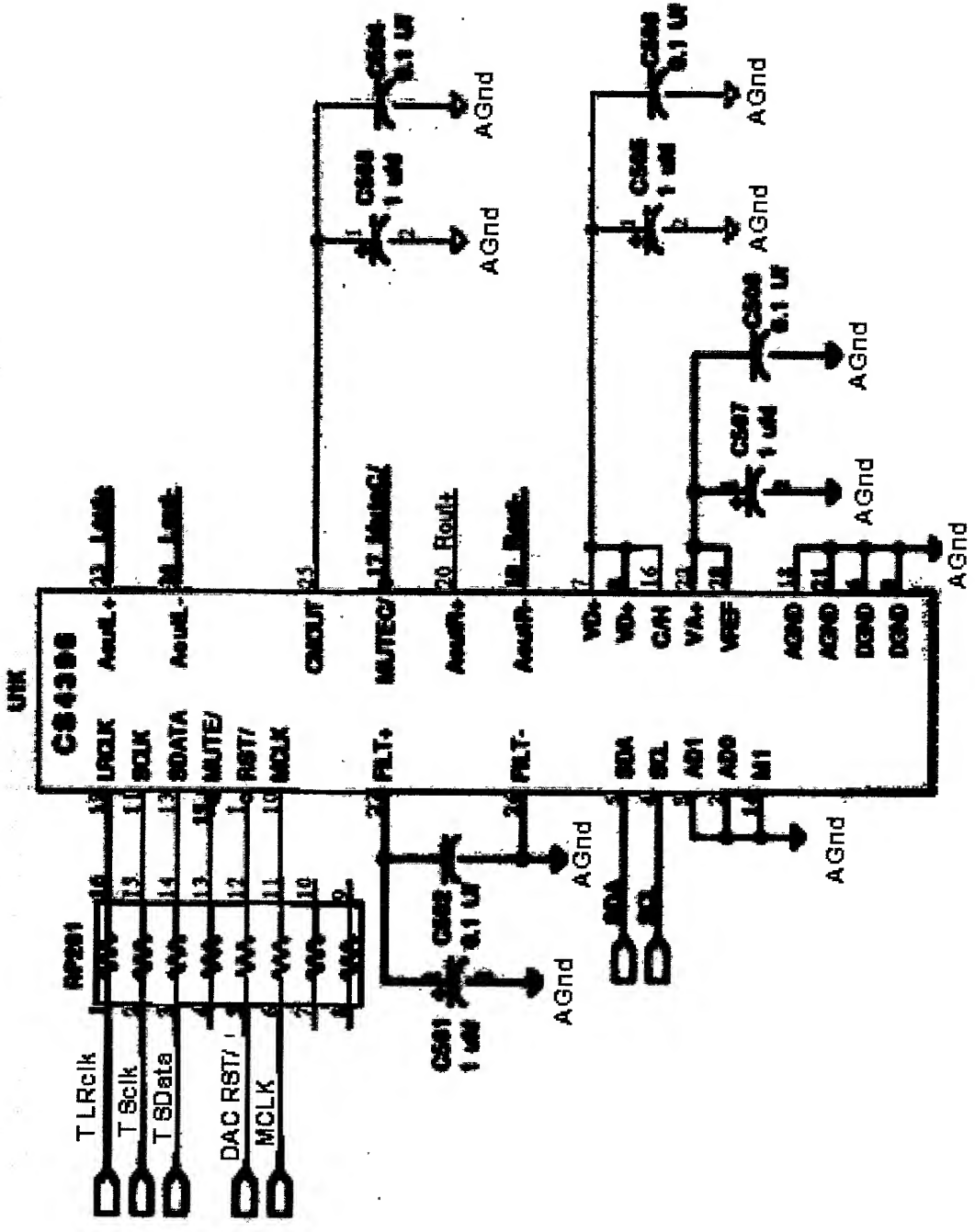
Audio Display Voice Over & Tone
Analog Audio Input
Fig. 7a



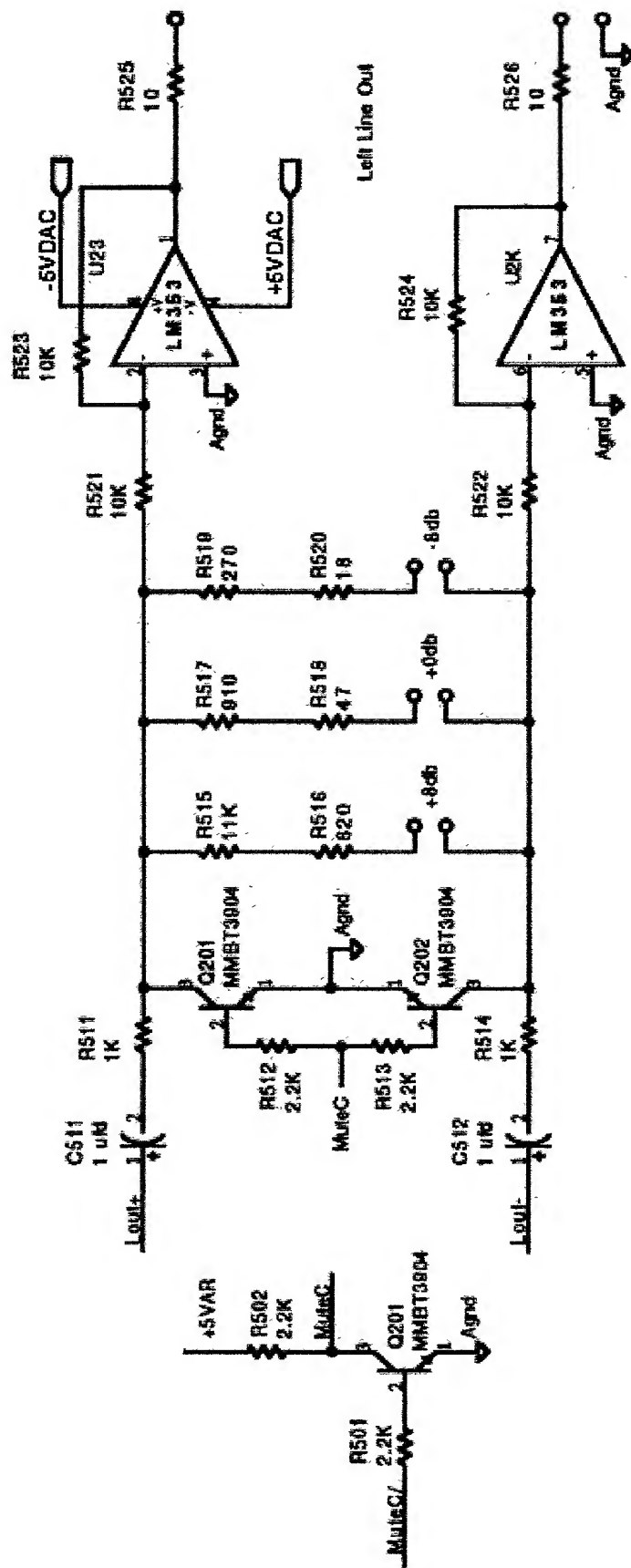
Audio Display Voice Over & Tone
Analog Audio Input
Fig. 7b



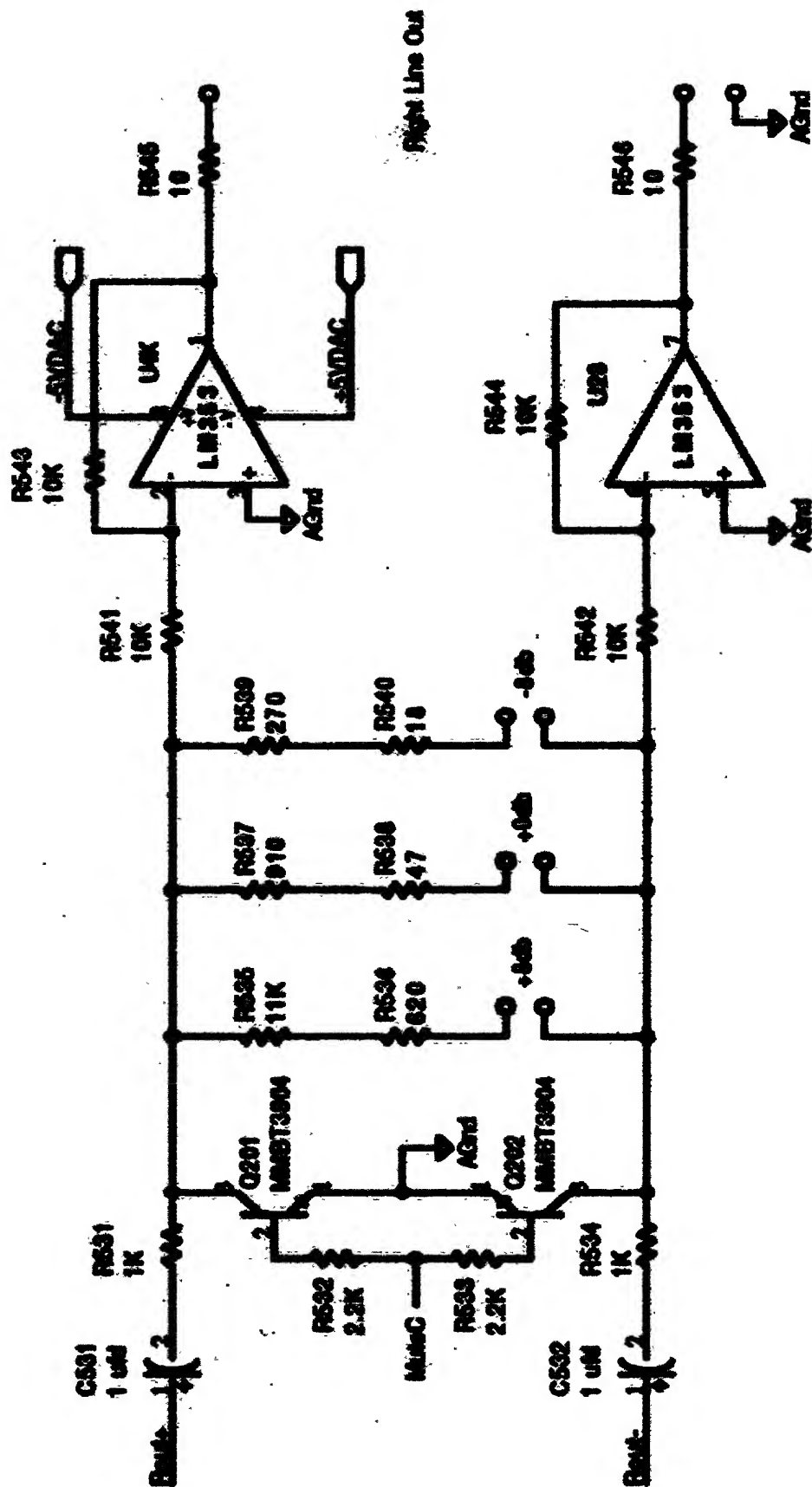
Audio Display Voice Over & Tone
Analog Audio Input
Fig. 7c



Audio Display Voice Over & Tone
Analog Audio Line Output
Fig. 8a



Audio Display Voice Over & Tone
Analog Audio Line Output
Fig. 8b



Audio Display Voice Over & Tone
Analog Audio Line Output
Fig. 8c